FIG. 1

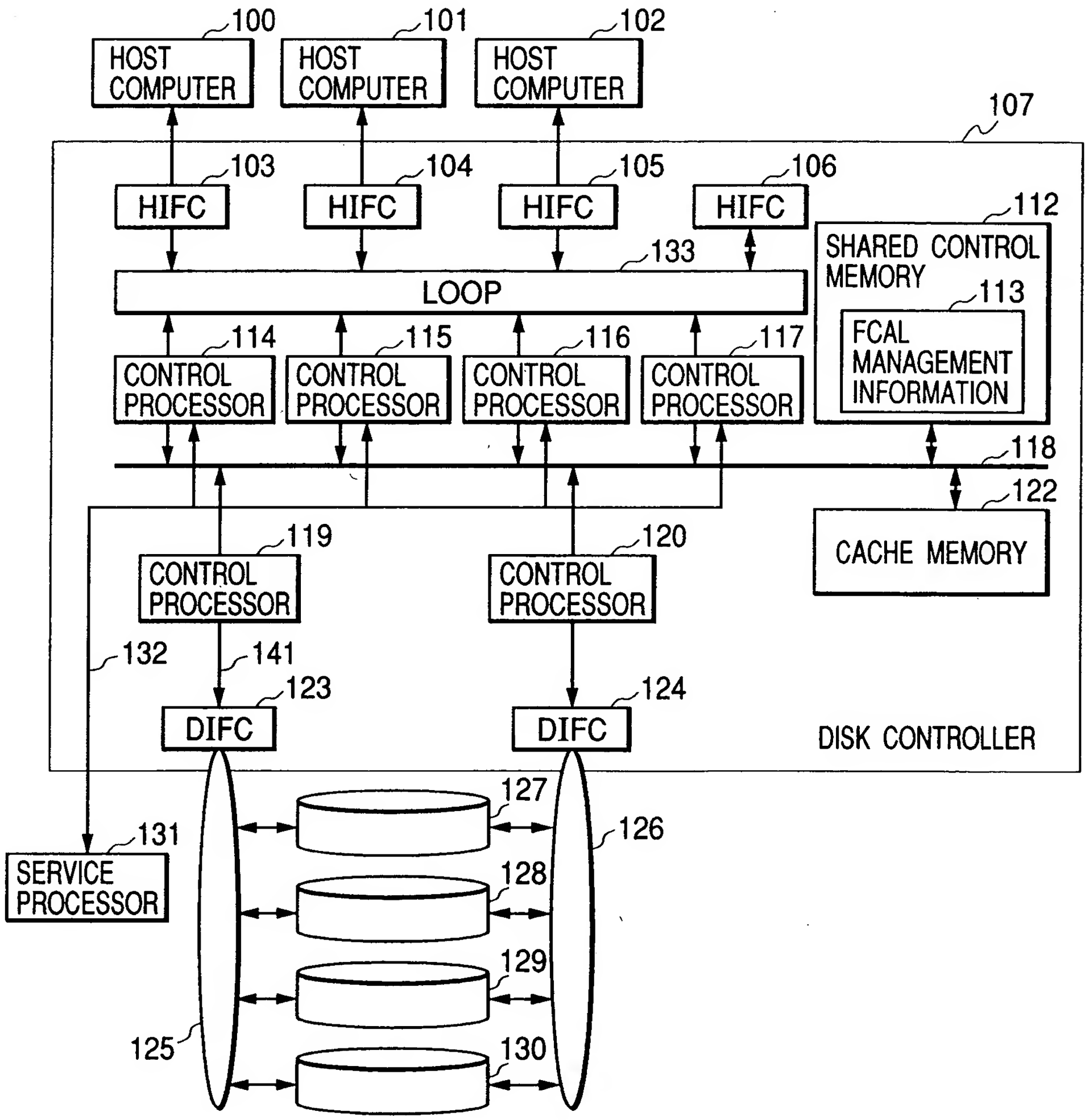

100
101
102
HOST COMPUTER
HOST COMPUTER
HOST COMPUTER
107
103
104
105
106
HIFC
HIFC
HIFC
HIFC
112
SHARED CONTROL MEMORY
113
FCAL MANAGEMENT INFORMATION
133
LOOP
114
115
116
117
CONTROL PROCESSOR
CONTROL PROCESSOR
CONTROL PROCESSOR
CONTROL PROCESSOR
118
122
CACHE MEMORY
119
120
CONTROL PROCESSOR
CONTROL PROCESSOR
132
141
123
124
DIFC
DIFC
DISK CONTROLLER
127
131
126
SERVICE PROCESSOR
128
129
125
130

FIG. 2

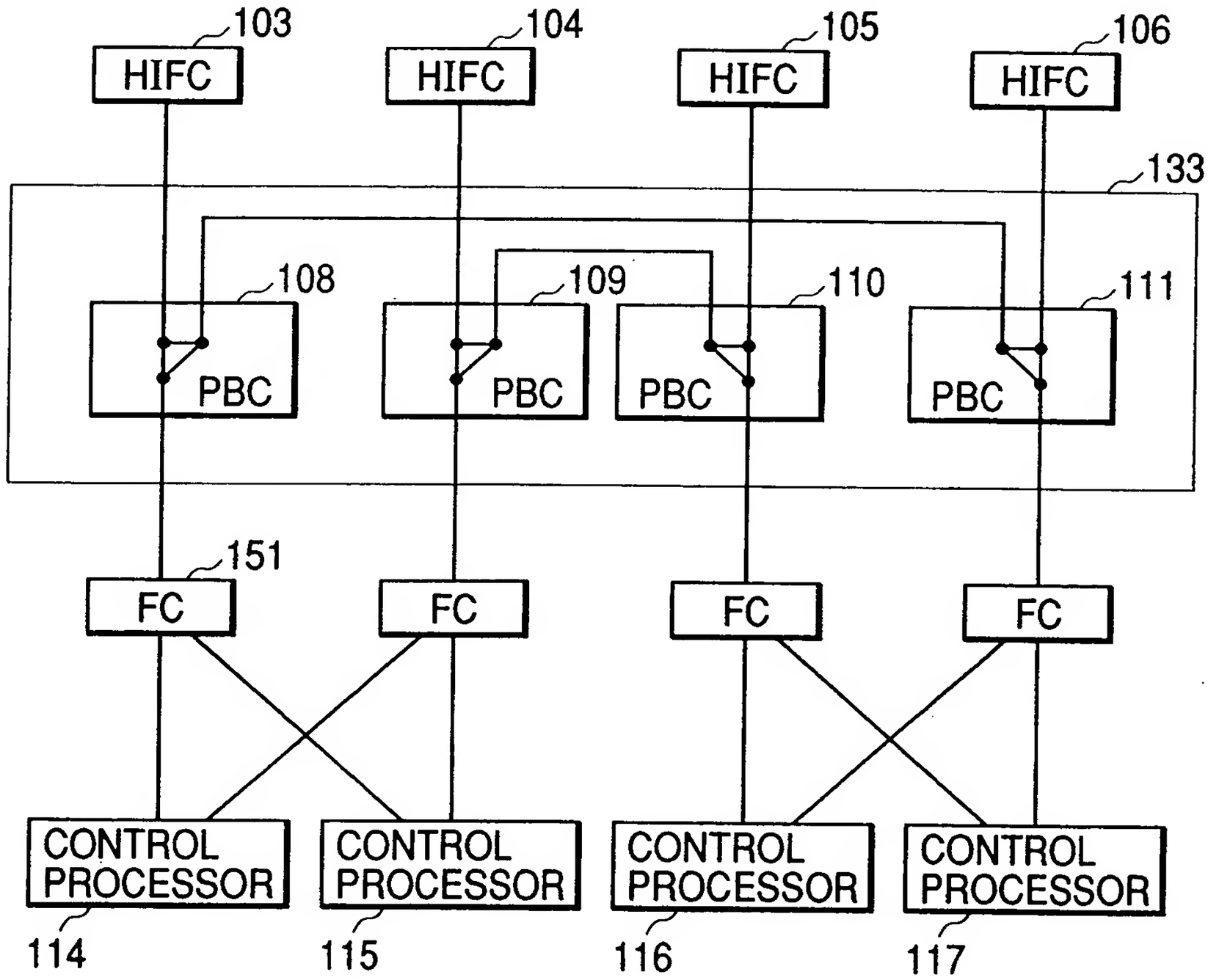

103
HIFC
104
HIFC
105
HIFC
106
HIFC
133
108
PBC
109
PBC
110
PBC
111
PBC
151
FC
FC
FC
FC
CONTROL PROCESSOR
114
CONTROL PROCESSOR
115
CONTROL PROCESSOR
116
CONTROL PROCESSOR
117

FIG. 3

FCAL MANAGEMENT INFORMATION 113

CONTROL PROCESSOR NUMBER (201)	AL-PA (202)	LUN (203)
114	EF	0—9
115	E8	10—19
116	E4	20—29
117	E2	30—39

FIG. 4

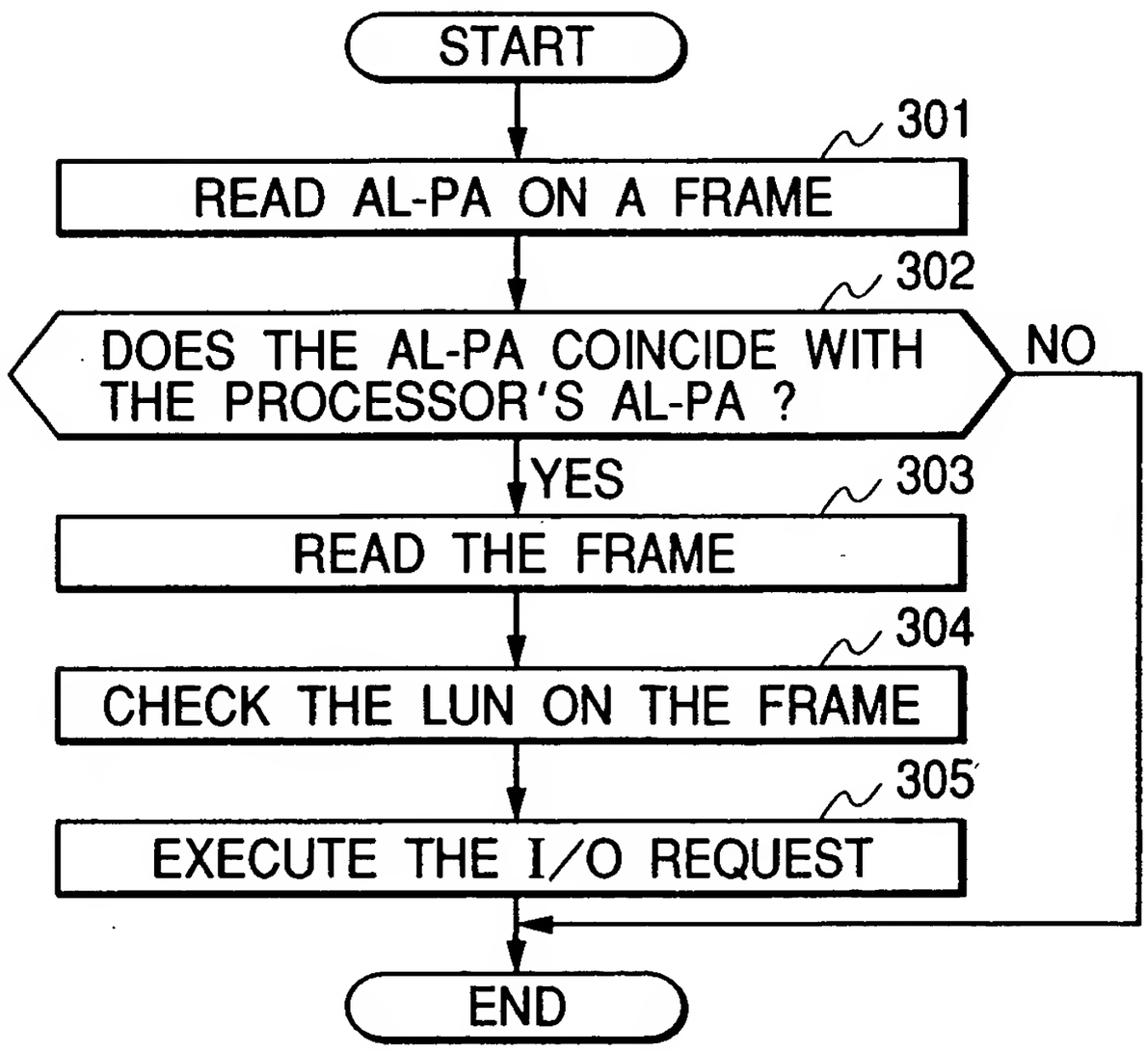


FIG. 5

FCAL MANAGEMENT INFORMATION 113

CONTROL PROCESSOR NUMBER (201)	AL-PA (202)	LUN (203)
114	EF	0—9
114	E8	10—19
115	—	—
116	E4	20—29
117	E2	30—39

FIG. 6

FCAL MANAGEMENT INFORMATION 113

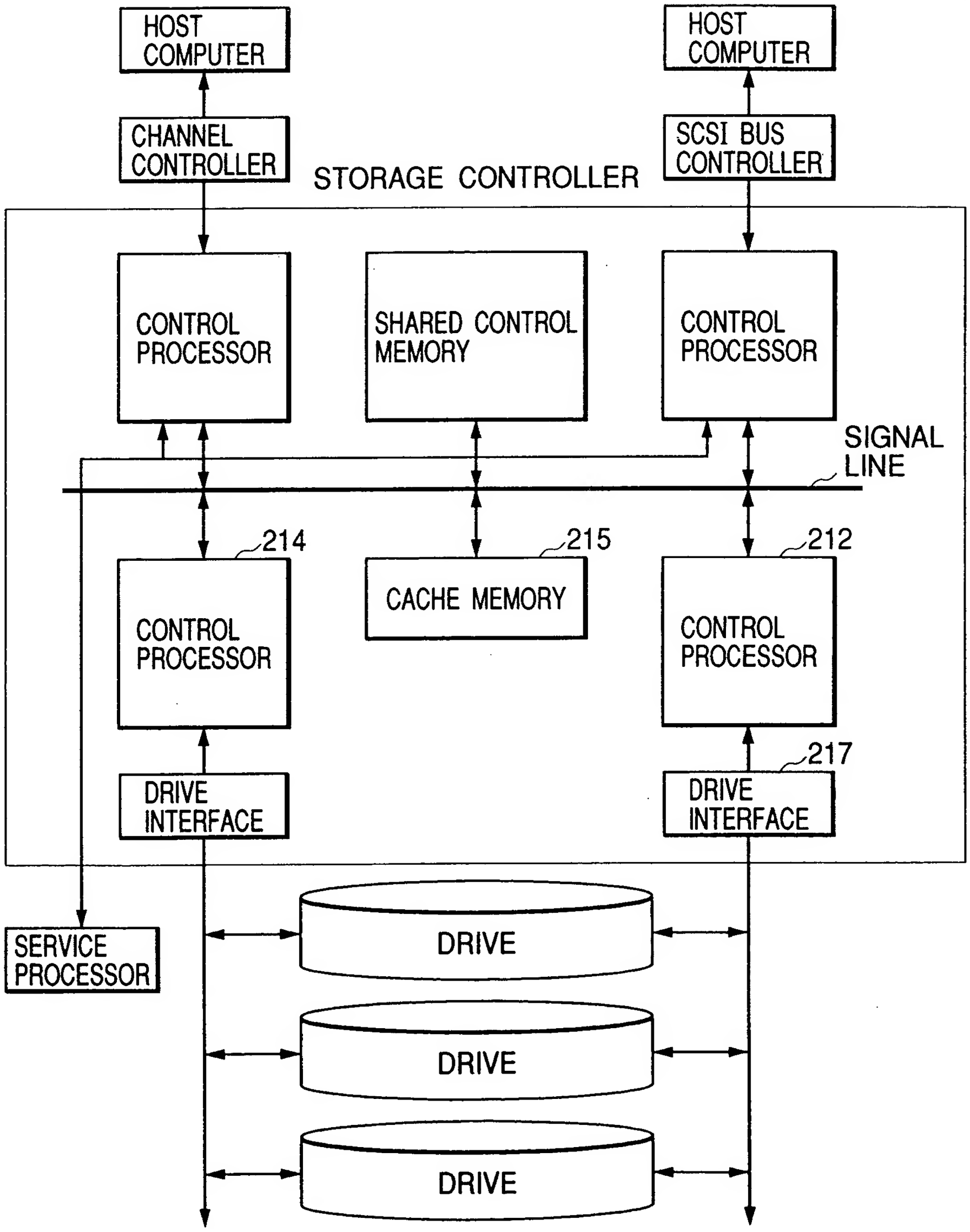
CONTROL PROCESSOR NUMBER (201)	AL-PA (202)	LUN (203)
114	EF	0—9
115	—	—
116	E8	10—19
116	E4	20—29
117	E2	30—39

FIG. 7

FCAL MANAGEMENT INFORMATION 113

CONTROL PROCESSOR NUMBER (201)	AL-PA (202)	LUN (203)
114	EF	0—9
115	E8	10—14
116	E4	15—29
117	E2	30—39

FIG. 8


HOST COMPUTER
HOST COMPUTER
CHANNEL CONTROLLER
SCSI BUS CONTROLLER
STORAGE CONTROLLER
CONTROL PROCESSOR
SHARED CONTROL MEMORY
CONTROL PROCESSOR
SIGNAL LINE
214
215
212
CONTROL PROCESSOR
CACHE MEMORY
CONTROL PROCESSOR
217
DRIVE INTERFACE
DRIVE INTERFACE
SERVICE PROCESSOR
DRIVE
DRIVE
DRIVE